FIG. 1

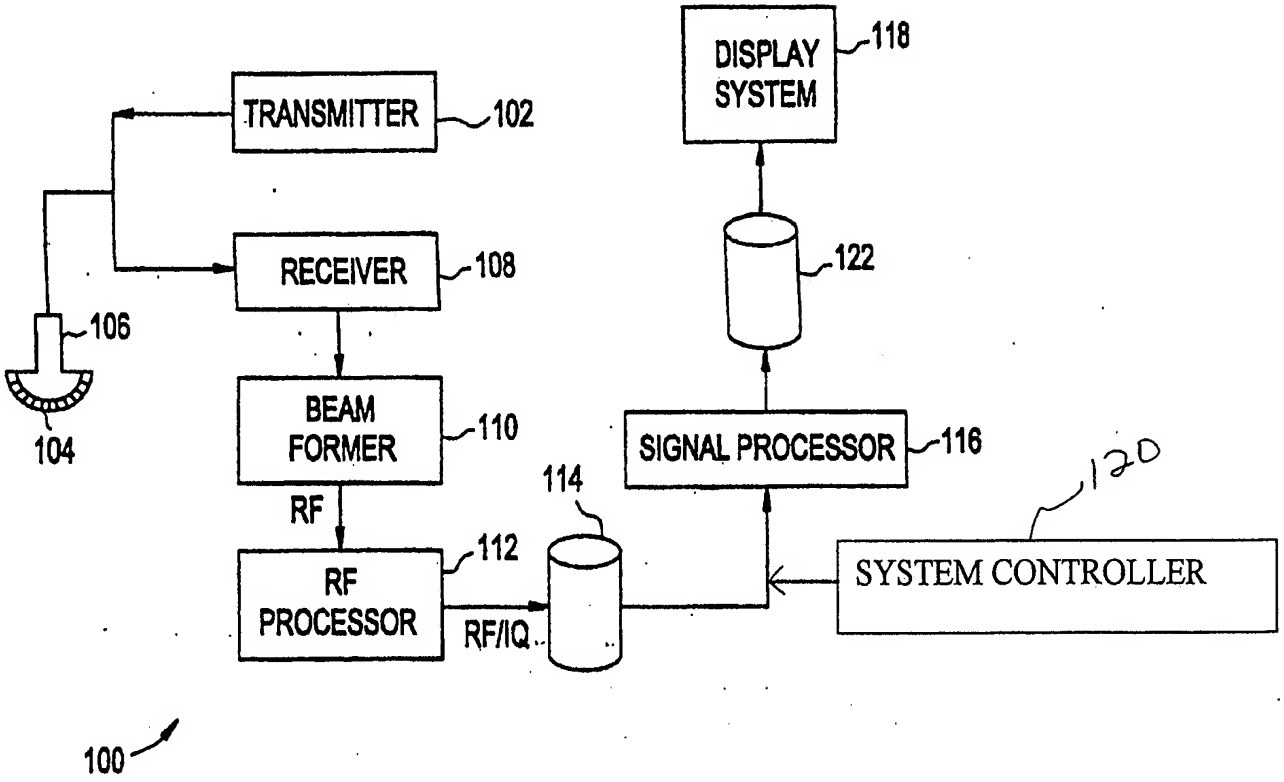
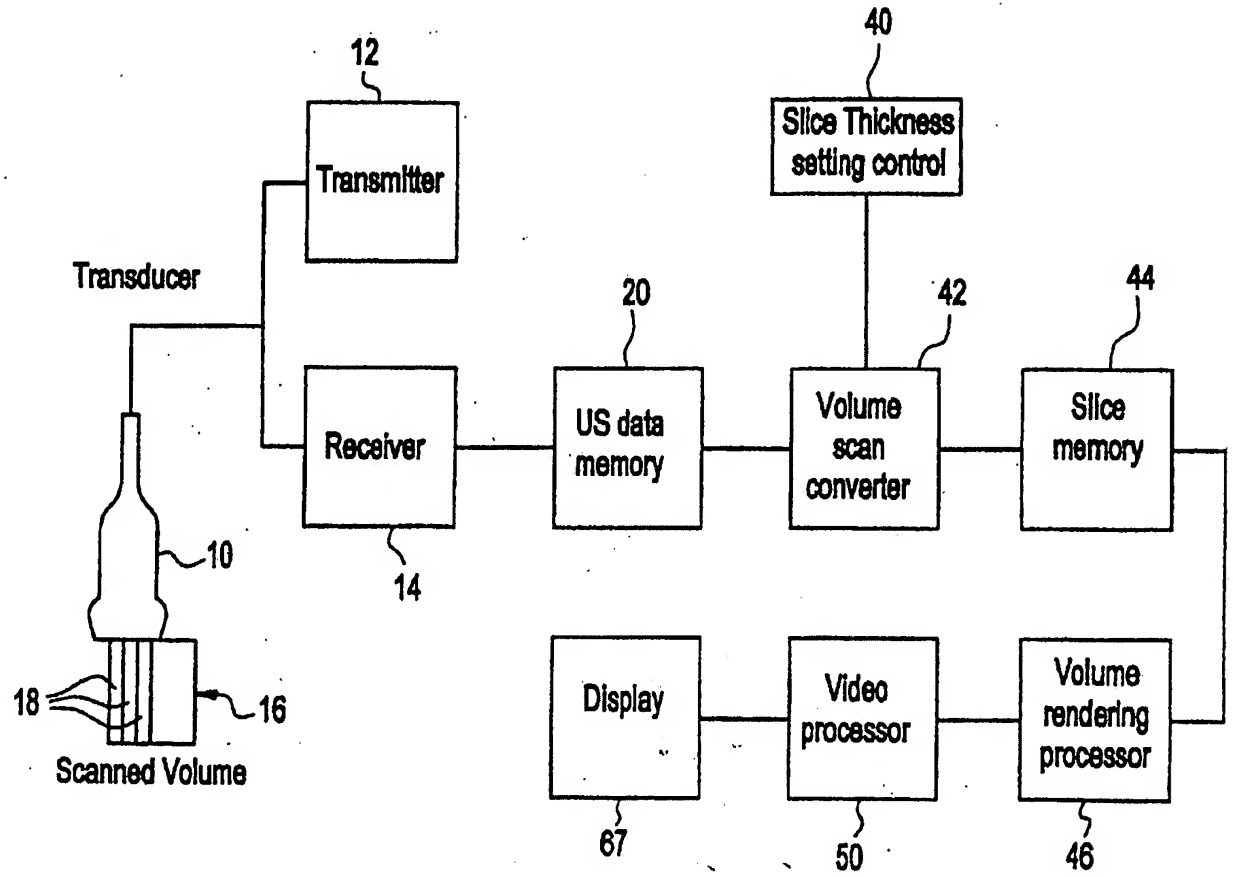

DISPLAY SYSTEM
118
TRANSMITTER
102
RECEIVER
108
106
104
BEAM FORMER
110
RF
RF PROCESSOR
112
114
RF/IQ
122
SIGNAL PROCESSOR
116
120
SYSTEM CONTROLLER
100

FIG. 2


12
Transmitter
40
Slice Thickness setting control
Transducer
20
42
44
Receiver
US data memory
Volume scan converter
Slice memory
10
14
18
16
Scanned Volume
Display
Video processor
Volume rendering processor
67
50
46

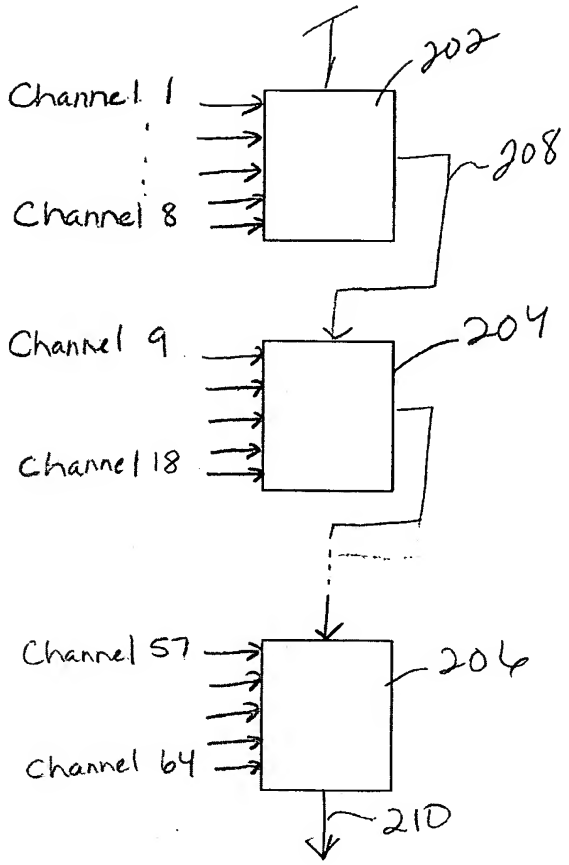


FIG. 3

PRIOR ART

FIG. 4

PRIOR ART

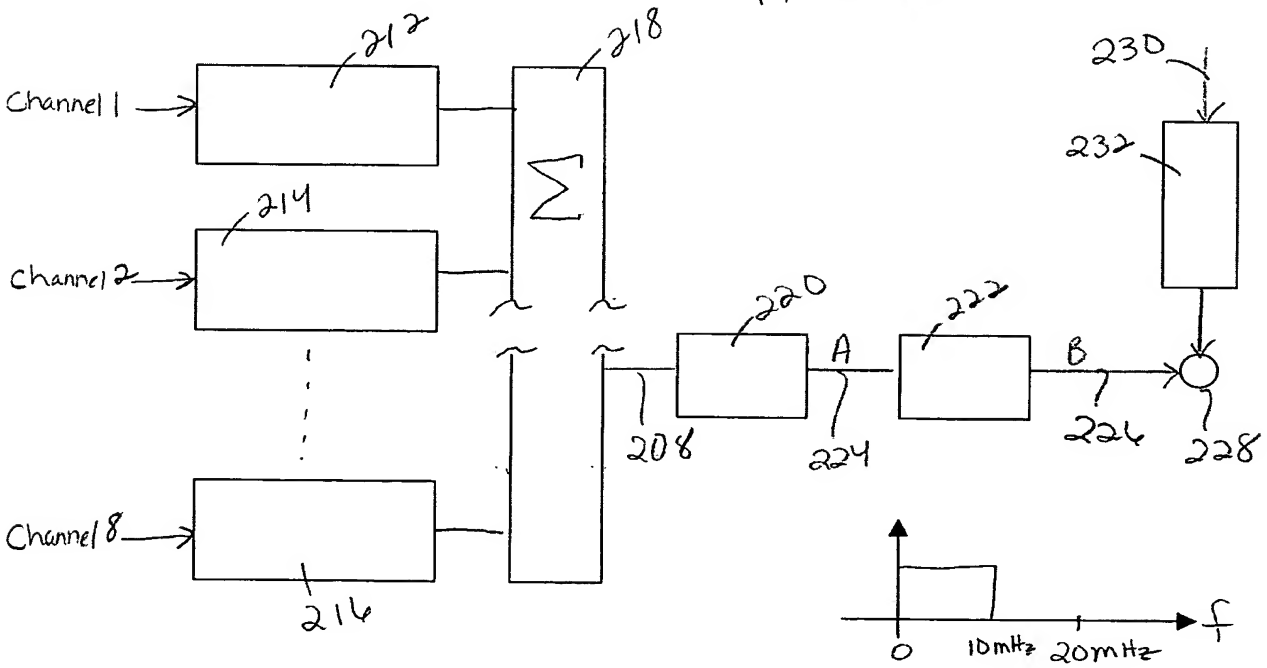
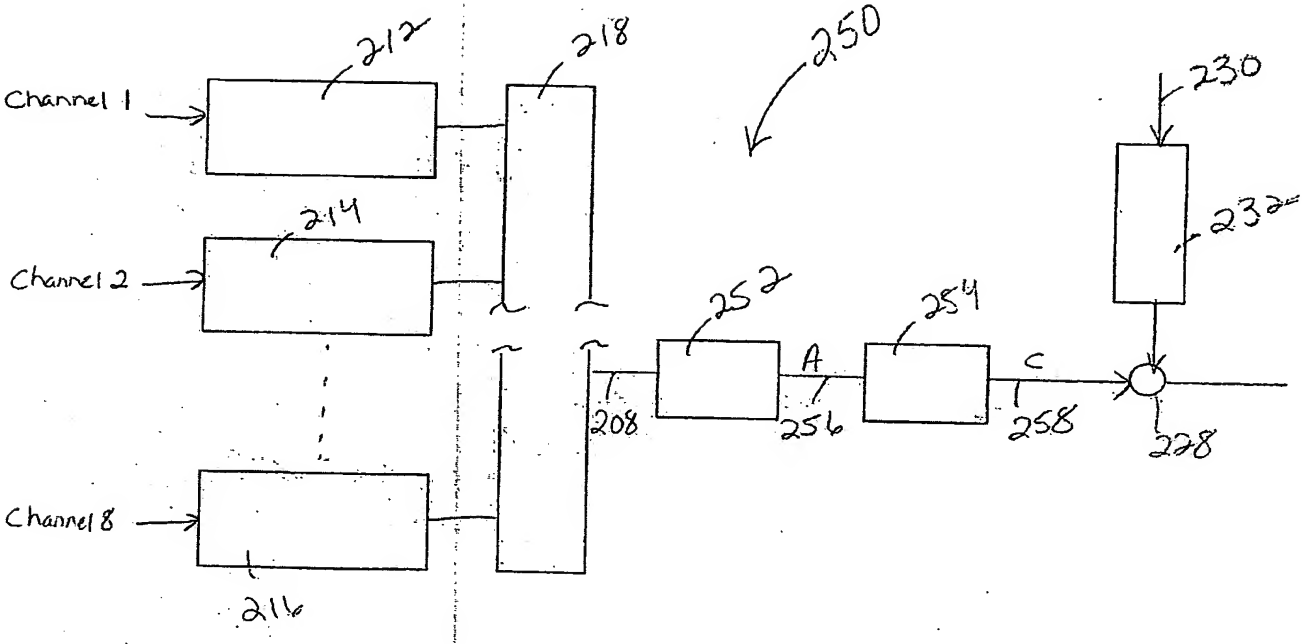


FIG. 5


Channel 1
Channel 2
Channel 8
212
214
216
218
250
230
232
252
254
A
C
208
256
258
228

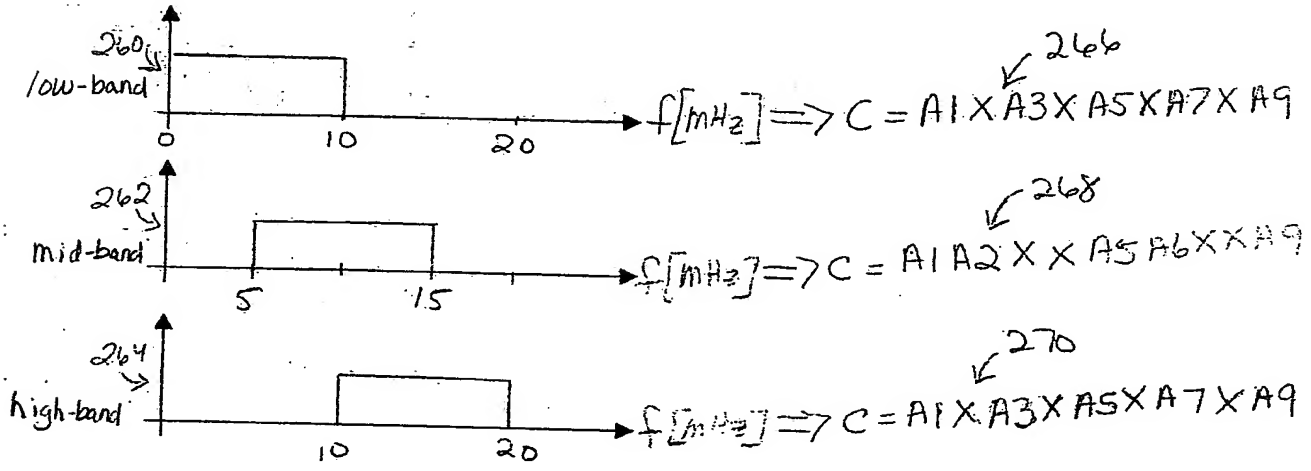
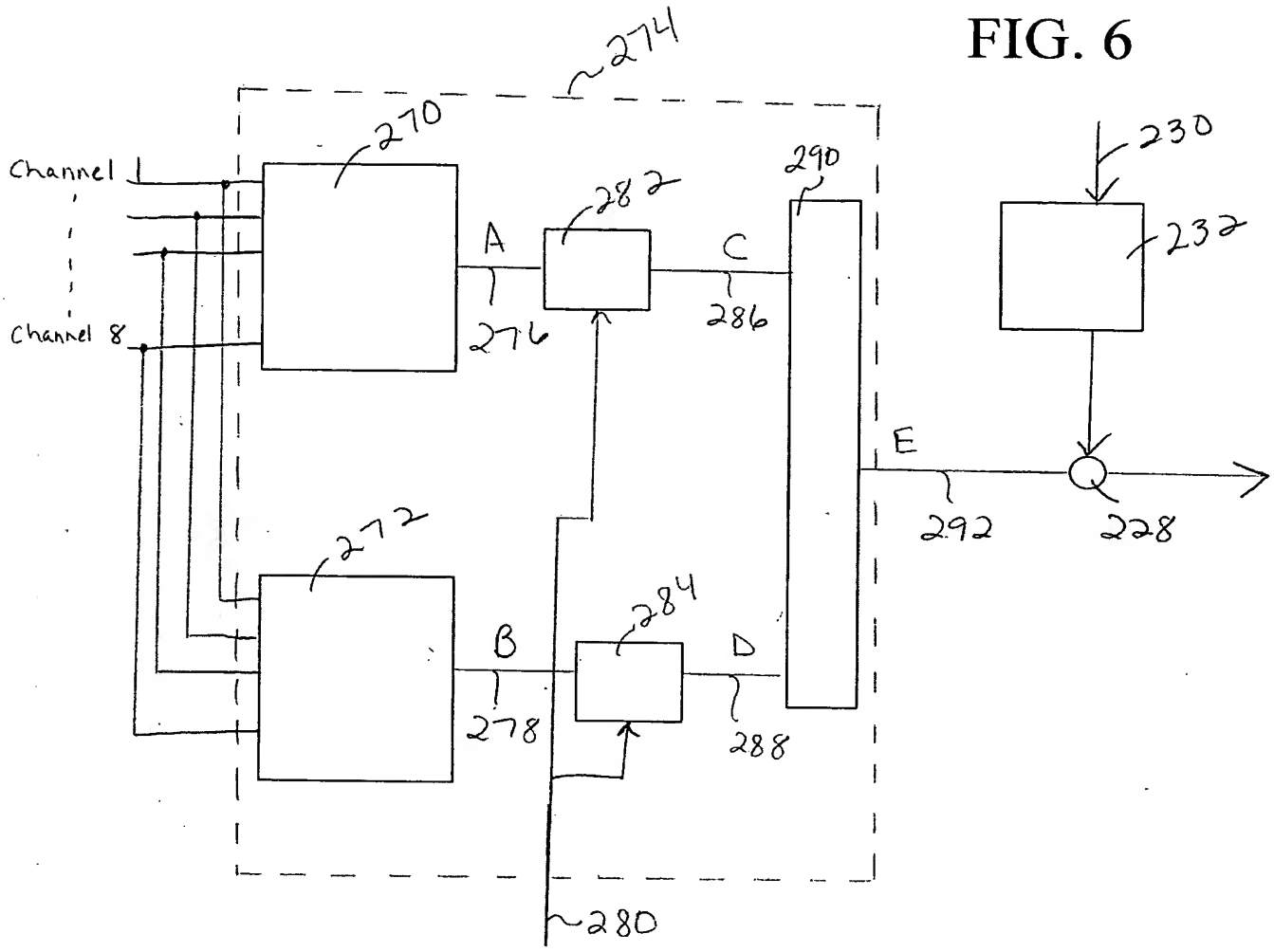

260
low-band
0
10
20
f[mHz] ⇒ C = A1 X A3 X A5 X A7 X A9
266
262
mid-band
5
15
f[mHz] ⇒ C = A1 A2 X X A5 A6 X X A9
268
264
high-band
10
20
f[mHz] ⇒ C = A1 X A3 X A5 X A7 X A9
270

FIG. 6



A	A1	A2	A3	A4	A5	A6	A7	A8	
B	B1	B2	B3	B4	B5	B6	B7	B8	
C	A1	A2	X	X	A5	A6	X	X	MID-BAND
D	B1	B2	X	X	B5	B6	X	X	
E	A1	A2	B1	B2	A5	A6	B5	B6	

FIG. 7